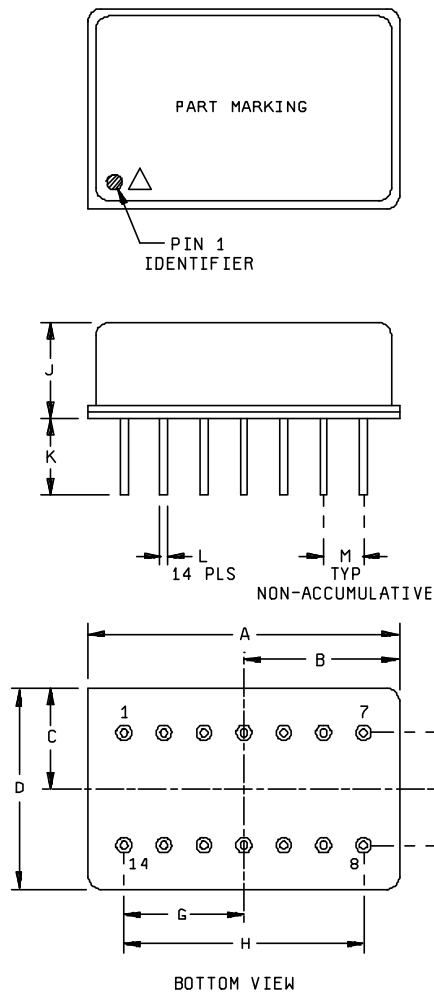


PERFORMANCE SPECIFICATION SHEET

OSCILLATOR, CRYSTAL CONTROLLED, TYPE 1 (CRYSTAL OSCILLATOR (XO)),
0.01 Hz THROUGH 15.0 MHz, HERMETIC SEAL, SQUARE WAVE, CMOS

This specification is approved for use by all Departments
and Agencies of the Department of Defense.

The requirements for acquiring the product described herein
shall consist of this specification sheet and [MIL-PRF-55310](#).



Pin number	Function
1	NC
2	NC
3	NC
4	NC
5	NC
6	NC
7	B - (GND/CASE)
8	OUTPUT
9	NC
10	NC
11	NC
12	NC
13	NC
14	B+

FIGURE 1. Dimensions and configuration.

Ltr	Inches		mm	
	Min	Max	Min	Max
A	---	.887	---	22.53
B	---	.44	---	11.2
C	---	.27	---	6.8
D	---	.54	---	13.7
E	.145	.155	3.68	3.94
F	.295	.305	7.49	7.75
G	.295	.305	7.49	7.75
H	.595	.605	15.11	15.37
J	---	.20	---	5.1
K	.20	---	5.1	---
L	.016	.020	0.41	0.51
M	.095	.105	2.41	2.67

NOTES:

1. Dimensions are in inches.
2. Metric equivalents are given for general information only.
3. Unless otherwise specified, tolerances are ± 0.005 (0.13 mm) for three place decimals and ± 0.02 (0.5 mm) for two place decimals.
4. All pins with NC function may be connected internally and are not to be used as external tie points or connections.
5. Color dot or square corner shall be used to indicate pin number 1.
6. ESD indicator, open triangle, shall be marked anywhere on the top of the oscillator.

FIGURE 1. Dimensions and configuration - Continued.

REQUIREMENTS:

Interface and physical dimensions: See figure 1.

Mounting: See figure 1.

Terminals: See figure 1.

Seal: Hermetic in accordance with [MIL-PRF-55310](#), maximum leakage rate 5×10^{-8} atm cc/s.

Weight: 0.5 ounce, maximum.

Oscillator: Class 2 or any class 1 or class 3 oscillator meeting all class 2 requirements and verification tests specified herein and in [MIL-PRF-55310](#).

Calibration: Manufacturer calibrated.

Screening: In accordance with [MIL-PRF-55310](#), product level B or S, as applicable.

Temperature:

Operating: See table I.

Storage: -62°C to +125°C.

Oscillator load: 10 kilohms ± 5 percent shunted by 15 pF ± 5 percent capacitor for CMOS compatible square.

Output waveform: Symmetrical square wave.

Output voltage: At designated CMOS load.

Logic 1: See table I.

Logic 0: See table I.

Rise and fall times: See table I.

Duty cycle: 40 to 60 percent duty cycle.

Supply voltage: See table I.

Input current: At designated supply voltage (see table I).

Output frequency: Frequency as designated at time of acquisition (see table I).

Initial accuracy at reference temperature (up to 30 days after shipment): See table I.

Initial frequency-temperature accuracy (one-half temperature cycle): Verification applicable. ^{1/}

Frequency-temperature tolerance (one-half temperature cycle, referenced to frequency measured at +23°C ± 1 °C, immediately prior to starting of the test): See table I. Measurements taken at ten equally spaced increments over the specified operating temperature range. ^{1/}

^{1/} For the purpose of transitioning this device to [MIL-PRF-55310](#), 'Frequency stability versus temperature' has been renamed 'Frequency-temperature tolerance'. The verification requirements of 'initial frequency-temperature accuracy (one-half temperature cycle)' shall apply except that frequency measurements shall be referenced to the frequency measured at +23°C ± 1 °C (f_{ref}) instead of to the nominal frequency (f_{nom})

TABLE I. Dash numbers and operating characteristics.

Dash number	Output frequency range	Supply voltage $\pm 10\%$	Maximum Input current at maximum supply voltage $\frac{1}{/}$	Output voltage		Rise and fall times (max)	Initial accuracy ppm at $+23^{\circ}\text{C}$ $\pm 1^{\circ}\text{C}$	Frequency aging ppm/year after 30 days	Frequency-temperature tolerance (ppm)		
				Logic: 1 (min)	Logic: 0 (max)				-55°C to +125°C	-55°C to +105°C	-20°C to +70°C
									A	B	C
01	0.01 Hz to 15.0 MHz	$\frac{V_{dc}}{+15}$	$\frac{mA}{25}$	$\frac{V_{dc}}{12.8}$	$\frac{V_{dc}}{1.5}$	$\frac{ns}{30}$	± 15	± 5	± 50	± 40	± 25
02		+15	25	12.8	1.5	30	± 25	± 10	± 100	± 80	± 50
11	0.01 Hz to 12.0 MHz	+12	20	10.2	1.2	35	± 15	± 5	± 50	± 40	± 25
12		+12	20	10.2	1.2	35	± 25	± 10	± 100	± 80	± 50
21	0.01 Hz to 10.0 MHz	+10	15	8.5	1.0	40	± 15	± 5	± 50	± 40	± 25
22		+10	15	8.5	1.0	40	± 25	± 10	± 100	± 80	± 50
31	0.01 Hz to 8.0 MHz	+8	10	6.8	0.8	50	± 15	± 5	± 50	± 40	± 25
32		+8	10	6.8	0.8	50	± 25	± 10	± 100	± 80	± 50
41	0.01 Hz to 5.0 MHz	+5	3	4.2	0.5	70	± 15	± 5	± 50	± 40	± 25
42		+5	3	4.2	0.5	70	± 25	± 10	± 100	± 80	± 50

$\frac{1}{/}$ Maximum input current for no load condition. Actual configuration of CMOS loads must be added to determine power supply requirements.

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Frequency-voltage tolerance: ± 2 ppm maximum for a ± 10 percent change in supply voltage. Measurements taken at reference temperature and operating temperature range end points.

Frequency aging: Measurements shall be taken at $+70^{\circ}\text{C} \pm 0.2^{\circ}\text{C}$ at intervals of not more than every 72 hours for 30 days minimum (see table I).

± 5 ppm per year, maximum

± 10 ppm per year, maximum

± 0.7 ppm per 30 days.
 ± 1.5 ppm per 90 days

± 1.5 ppm per 30 days
 ± 3 ppm per 90 days

Terminal strength: [Method 211 of MIL-STD-202](#), test condition C.

Applied force: 2 pounds each terminal for 10 seconds.

Bends: Five at 45 degrees each.

Frequency-environmental tolerance: Not applicable.

Vibration, sinusoidal: In accordance with [MIL-PRF-55310](#) and [method 204 of MIL-STD-202](#).

Nonoperating: Test condition D.

Operating: Not required.

Ambient pressure:

Nonoperating: In accordance with [MIL-PRF-55310](#).

Operating: [Method 105 of MIL-STD-202](#), test condition C.

Part or Identifying Number (PIN): Consists of "M" prefix followed by specification sheet number, a dash and coded alphas, and numeric number. See example:

EXAMPLE

M55310/18- S 01 A XXXXXXXX

M prefix and specification sheet number

Product level (S, B, or C)

Dash number (see table I)

Operating temperature range
(A, B, or C) (see table I)

Frequency

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Reference documents. In addition to [MIL-PRF-55310](#), this document references the following:

[MIL-STD-202](#)

The margins of this specification are marked with vertical lines to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Custodians:

Army - CR
Navy - EC
Air Force - 99
DLA - CC

Preparing activity:

Army - CR

Agent:

DLA - CC

Review activities:

Army - AR, MI, SM
Navy - AS, CG, MC
Air Force - 19, 84
NASA - NA

(Project 5955-2009-018)

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at <http://assist.daps.dla.mil>.