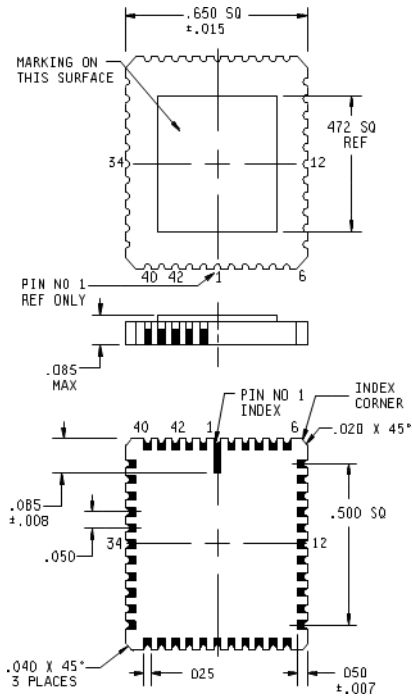


PERFORMANCE SPECIFICATION SHEET

OSCILLATOR, CRYSTAL CONTROLLED, TYPE 1 (CRYSTAL OSCILLATOR (XO)),
40.0 KHZ THROUGH 60.0 MHZ, HERMETIC SEAL, SQUARE WAVE, TTL

This specification is approved for use by all Departments
and Agencies of the Department of Defense.

The requirements for acquiring the product described herein
shall consist of this specification sheet and [MIL-PRF-55310](#).



Pin number	Function
1-5	NC
6	B+
7-11	NC
12	B+
13-33	NC
34	GND
35-39	NC
40	GND
41	NC
42	OUTPUT

Inches	mm	Inches	mm
.007	0.18	.085	2.16
.008	0.20	.472	11.99
.015	0.38	.500	12.70
.025	0.64	.650	16.51
.050	1.27		

NOTES:

1. Dimensions are in inches.
2. Metric equivalents are given for general information only.
3. Unless otherwise specified, tolerances are ± 0.005 (0.13 mm) for three place decimals and ± 0.02 (0.5 mm) for two place decimals.
4. All pads with NC function may be connected internally and are not to be used as external tie points or connectors.

FIGURE 1. Dimensions and configuration.

REQUIREMENTS:

Interface and physical dimensions: See figure 1.

Mounting: See figure 1.

Pads: See figure 1.

Seal: Hermetic in accordance with [MIL-PRF-55310](#), maximum leakage rate 5×10^{-8} atm cc/s.

Weight: 4 grams, maximum.

Oscillator: Class 2 or any class 1 or class 3 oscillator meeting all class 2 requirements and verification tests specified herein and in [MIL-PRF-55310](#).

Calibration: Manufacturer calibrated.

Screening: In accordance with [MIL-PRF-55310](#), product level B or S, as applicable.

Temperature:

Operating: See table I.

Storage: -62°C to +125°C.

Oscillator load: Standard TTL loads (see table I).

Output waveform: Symmetrical square wave.

Output voltage:

Logic 1: 2.4 V dc, minimum.

Logic 0: 0.5 V dc, maximum.

Rise and fall times: See table I.

Duty cycle: See table I.

Supply voltage: 5.0 V dc ± 10 percent.

Input current: At designated supply voltage (see table I).

Output frequency: Frequency as designated at time of acquisition (see table I).

Initial accuracy at reference temperature (up to 30 days after shipment): See table I.

Initial frequency-temperature accuracy (one-half temperature cycle): Verification applicable. ^{1/}

Frequency-temperature tolerance (one-half temperature cycle, referenced to frequency measured at +23°C ± 1 °C, immediately prior to starting of the test): See table I. Measurements taken at ten equally spaced increments over the specified operating temperature range. ^{1/}

^{1/} For the purpose of transitioning this device to [MIL-PRF-55310](#), 'Frequency stability versus temperature' has been renamed 'Frequency-temperature tolerance'. The verification requirements of 'initial frequency-temperature accuracy (one-half temperature cycle)' shall apply except that frequency measurements shall be referenced to the frequency measured at +23°C ± 1 °C (f_{ref}) instead of to the nominal frequency (f_{nom}).

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TABLE I. Dash numbers and operating characteristics.

Dash number	Output frequency range	Input current (max) at 5.25 V $\pm 1\%$ <u>1/</u>	Pulse characteristics			Initial accuracy ppm at +23°C $\pm 1^\circ\text{C}$	Frequency aging ppm/year after 30 days	Frequency-temperature tolerance (ppm)		
			Rise and fall times (max)	Duty cycle at 1.4 V	Load (max) <u>2/</u>			-55°C to +125°C	-55°C to +105°C	-20°C to +70°C
								A	B	C
01	40 kHz	<u>Ma</u> 75	<u>ns</u> 15	<u>percent</u> 40 to 60	10 TTL	± 15	± 10	± 50	± 40	± 25
02	to	75	15	40 to 60	10 TTL	± 25	± 10	± 100	± 80	± 50
03	14.0 MHz	75	15	40 to 60	10 TTL	± 25	± 10	± 200	± 150	± 100
11	14.0 MHz	30	5	40 to 60	10 TTL	± 15	± 10	± 50	± 40	± 25
12	to	30	5	40 to 60	10 TTL	± 25	± 10	± 100	± 80	± 50
13	30.0 MHz	30	5	40 to 60	10 TTL	± 25	± 10	± 200	± 150	± 100
21	30.0 MHz	55	5	40 to 60	10 TTL	± 15	± 10	± 50	± 40	± 25
22	to	55	5	40 to 60	10 TTL	± 25	± 10	± 100	± 80	± 50
23	60.0 MHz	55	5	40 to 60	10 TTL	± 25	± 10	± 200	± 150	± 100

1/ Maximum input current for no load condition. Actual configuration of TTL loads must be added to determine power supply requirements.

2/ A TTL unit load is defined as: 1.6 mA sink, 0.04 mA source, and 2pF capacitance.

Frequency-voltage tolerance: ± 4 ppm maximum for a ± 10 percent change in supply voltage. Measurements taken at reference temperature and operating temperature range end points.

Frequency aging: Measurements shall be taken at +70°C $\pm 0.2^\circ\text{C}$ at intervals of not more than every 72 hours for 30 days minimum (see table I).

± 10 ppm per year, maximum

± 1.5 ppm per 30 days.

± 3 ppm per 90 days.

Frequency-environmental tolerance: Not applicable.

Vibration, sinusoidal: In accordance with [MIL-PRF-55310](#) and [method 204 of MIL-STD-202](#).

Nonoperating: Test condition G.

Operating: Not required.

Ambient pressure:

Nonoperating: In accordance with [MIL-PRF-55310](#).

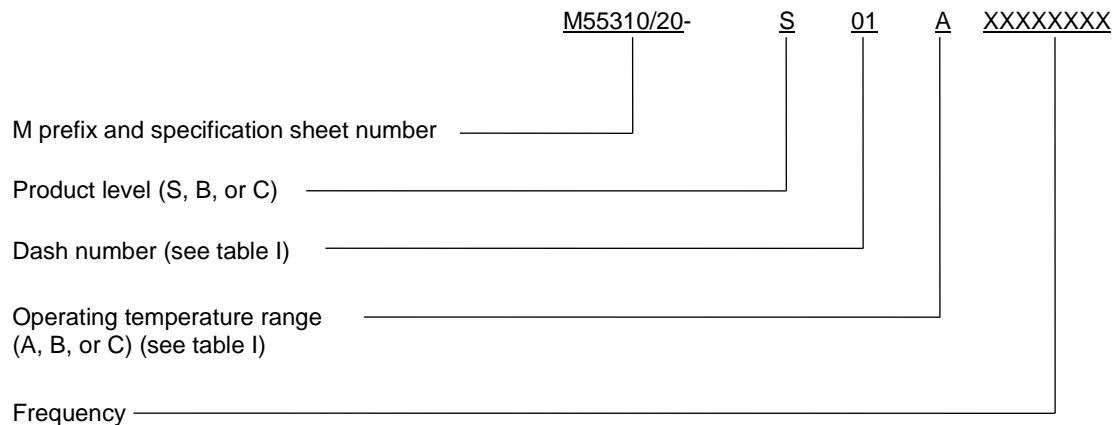
Operating: [Method 105 of MIL-STD-202](#), test condition C.

Reflow soldering: Reflow soldering of the unit at +230°C $\pm 10^\circ\text{C}$ for 15 seconds shall not degrade the performance.

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Part or Identifying Number (PIN): Consists of "M" prefix followed by specification sheet number, a dash and coded alphas, and numeric number. See example:

EXAMPLE



Reference documents. In addition to [MIL-PRF-55310](#), this document references the following:

[MIL-STD-202](#)

The margins of this specification sheet are marked with vertical lines to indicate where changes from the previous issue were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous issue.

Custodians:
Air Force - 99
DLA - CC

Preparing activity:
DLA - CC

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Review activities:
Air Force - 19, 84

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